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FORM PTO-1449	SERIAL NO.	CASE NO.
	10/023,200	10519/24
LIST OF PATENTS AND PHENE ATIONS FOR	FILING DATE	GROUP ART UNIT
APPLICANT'S INFORMATION DISCLOSURE	December 14, 2001	2133 2818
STATEMENT		2100
(use several sheets if necessary)	APPLICANT(S): MARCH ET AL.	

REFERENCE	DESIG		U.S. PATEN	IT DOCUMENTS	CLACCI	FII 110
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
ome	A1	6,236,587	05/22/01	GUDESEN ET AL.		
	A2	6,034,882	03/07/00	JOHNSON ET AL.		
	A3	5,835,396	11/10/98	ZHANG	,	
	A4	5,784,391	07/21/98	KONIGSBURG		
	A5	5,469,450	11/21/95	CHO ET AL.		
	A6	5,313,425	05/17/94	LEE ET AL.		
	A7	4,646,266	. 02/24/87	OVSHINSKY ET AL.	-	
	A8	6,216,247	04/10/01	CRETA ET AL.		
	A9	6,185,122	02/06/01	JOHNSON ET AL.		
	A10	6,016,269	01/18/00	PETERSON ET AL.		
	A11	5,943,254	08/24/99	BAKEMAN, JR. ET AL.		
	A12	5,835,509	11/10/98	SAKO ET AL.		
	A13	5,796,694	08/18/98	SHIRANE		
	A14	5,708,667	01/13/98	HAYASHI		
11	A15	5,432,729	07/11/95	CARSON ET AL.		
Pm	A16	5,872,790	02/16/99	DIXON		

FOREIGN PATENT DOCUMENTS

١	EXAMINER		DOCUMENT			CLASS/	TRANS	LATION
ı	INITIAL	,	NUMBER	DATE	COUNTRY ·	SUBCLASS	YES	NO
1		A17	WO 99/14763	03/25/99	PCT			

EXAMINER INITIAL		OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)
bure	A18	"Computer Engineering: Hardware Design," M. Morris Mano, Chapter 6-4 Error Detection and Correction, pages 199-202 (1988).
me	A19	"Reed-Solomon Codes," http://www.4i2i.com/reed_solomon_codes.htm, 8 pages (1998).
gm-	A20	"The Norton Desktop (Version 3 For Windows) User's Guide," pp. 16-1 - 16-6 (1993).
pre	A21	"Exotic Memories, Diverse Approaches," EDN Asia, pages 22-33 (September 2001).
ame	A22	"A Vertical Leap for Microchips," Thomas H. Lee, Scientific American, 8 pages (January 2002; printed December 10, 2001).
	A23	"Three-Dimensional Memory Array and Method of Fabrication," U.S. Patent Application Serial
		No. 09/560,626, filed April 28, 2000; inventor: Johan-Knall.
	A24	"Write-Once Memory Array Controller, System, and Method," U.S. Patent-Application-Serial
		No. 09/638,427, filed August 14, 2000; inventors: Derek J. Bosch, Christopher S. Moore,
	İ	Daniel C. Steere, and J. James Tringali.

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FORM PTO-1449	MAR 1 2 2002 &	SERIAL NO. 10/023,200 FILING DATE	CASE NO. 10519/24 GROUP ART UNIT
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EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.) A25 "Low-Cost Three-Dimensional Memory Array," U.S. Patent Application Serial No. 09/638,42: A25 "Low-Cost Three-Dimensional Memory Array," U.S. Patent Application Serial No. 09/638,42:	8,
	filed August 14, 2000; inventors: Mark 5, Johnson, Hours 31, 200, 110	
	P. Michael Farmwald. A26. "Modular Memory Device," U.S. Patent Application Serial No. 09/638,334, filed August 14,	
	A26. "Modular Memory Device," U.S. Patent Application Genal No. 397-500, 304, Most Register and A26. "Modular Memory Device," U.S. Patent Application Genal No. 397-500, 304, Most Register and A26. "Modular Memory Device," U.S. Patent Application Genal No. 397-500, 304, Most Register and A26. "Modular Memory Device," U.S. Patent Application Genal No. 397-500, 304, Most Register and A26. "Modular Memory Device," U.S. Patent Application Genal No. 397-500, 304, Most Register and A26. "Modular Memory Device," U.S. Patent Application Genal No. 397-500, 304, 304, 304, 304, 304, 304, 304, 3	L
1		
	and Derek J. Bosch. A27 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A27 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A27 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A28 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A29 "Memory Devices and Methods for Use Therewith," U.S. Patent Application Serial No. A	
		n
	A28 "Three-Dimensional Memory Array and Method for Storting Data bits and 200 bits LLS Ratent Application Serial No. 09/747,574, filed December 22, 2000; inventors: Thomas	s, H.
	Mork C. Johnson	
	A29 **Method for Deleting Stored Digital Data from Write-Once Memory Device, Stored No. 09/638-439, filed August 14, 2000; inventors: Christopher S. Moore,	
	I Desire Constant I Tames Tribitation	
	A30 "Solid State Memory Device Storing Program Code and Methods for use Interesting C	
	Moore, Roger March, Dan Brown.	

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LIST OF PATENT	S AND PUBLICATIONS FOR	FILING DATE	GROUP ART UNIT
APPLICANT'S IN	IFORMATION DISCLOSURE	December 14, 2001	2818
5	STATEMENT		2133
(use several sheets if ne	ecessary)	APPLICANT(S): ROGER W. MA	RCH ET AL.

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
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FOREIGN PATENT DOCUMENTS

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